



Join Accurision to drive innovation in the field of high integrity, high continuity satellite-based positioning and timing solutions. We are looking for a highly motivated FPGA engineer with technical fundamentals in GNSS who is passionate about building technology that serves our customers.

FPGA Engineer (f/m)

Accurision requires a self-motivated individual to help design and implement the digital hardware for our advanced GNSS sensors. You will drive innovation in the digital design and collaborate closely with other GNSS team members. You will help develop the next generations of high integrity, high continuity positioning and timing solutions.

You need to have an EU citizenship.

Tasks

- Definition, design, development, implementation, and testing of the digital hardware of our GNSS sensors
- Analysis and continuous optimisation of the existing HDL to increase stability, scalability, maintainability, testability, and quality
- Automation of complete execution from development to system test and KPI analysis

Skills & Experience

- A University or University of Applied Sciences degree in Electronic Engineering or Computer Science with 2+ years of professional experience or comparable education/professional experience
- Digital logic design (FPGA)
- HDL Programming (Verilog and/or VHDL)
- C/C++ and scripting languages (Python, TCL)
- Good aptitude for problem solving and logical reasoning
- Experience with GNSS (BeiDou, Galileo, GPS) is a plus
- High proficiency in English, German is a plus

Our Offer

- Exciting and challenging tasks in the field of high integrity, high continuity satellite-based positioning and timing solutions
- Opportunity to play a decisive role in the development of our GNSS sensor platform
- Small/international team
- 4-day work week (Monday through Thursday)

Please send your application to: office@accurision.com

Compulsory note: The actual salary is above the Austrian IT collective agreement and is based on skills and experience.